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09/932,704 Byers 41-3

SEP 2 4 2005

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

## **Patent Application**

Inventor(s):

Byers et al.

Case No.: Serial No.:

Filing Date:

Byers 41-3

Byers 41-3

09/932,704 08/17/2001 Examiner:

Agustin Bello

Art Unit:

2633

Title:

Interconnecting Processing Units of a Stored Program Controlled System Using Time

Division Multiplexed Free Space Optics

I hereby certify that this correspondence is being facsimile transmitted to the United States

Patent and Trademark Office on September 24, 2005.

Charles L. Warren

COMMISSIONER FOR PATENTS

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SIR:

## Amendment Responding to the Office Action Made Final

This communication is responsive to the Office Action of July 27, 2005 that set a 3 month date for response of October 27, 2005 with a 2 month date of September 27, 2005. Thus, this response is timely filed within the 2 month date.